

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Johannes Becker
Serial No.: 10/706,365
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Title: SYSTEM AND METHOD FOR SECURING AN INTEGRATED
CIRCUIT AS AGAINST SUBSEQUENT REPROGRAMMING
Grp./A.U.: 2435
Examiner: Suman Debnath
Confirmation No.: 6816

Commissioner for Patents
P.O. Box 1450
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I hereby certify that this correspondence is being electronically filed with United States Patent and Trademark Office on:	
October 29, 2009	(Date)
Jana R. Williford	
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/Jana R. Williford/	
(Signature of the person signing the certificate)	

Sir:

AMENDMENT SUBMITTED WITH RCE UNDER 37 C.F.R. §1.114(c)

Before continuing examination of the above-identified application, please refer to the below arguments and amend the claims as indicated.